

Amendments to the Specification:

Please amend the specification as follows:

Please replace paragraph starting at page 2, line 1-6, with the following rewritten paragraph:

a' Specifically, this prior art disk array apparatus 101, that communicates with the upper-level host system 102, has such a configuration as shown in Fig. 3 to implement cache redundancy. The cache redundancy is implemented in this configuration by previously copying (mirroring) the same data via an internal data bus 112 in both cache modules 109 and 110.